AN5276

5 W \times 2-ch (19V, 8 Ω) power amplifier with variable audio output and volume control

■ Overview

The AN5276 is a monolithic integrated circuit designed for 5.0 W (19 V, 8 Ω) output audio power amplifier. It is a dual channel SEPP IC suitable for stereo operation in TV application.

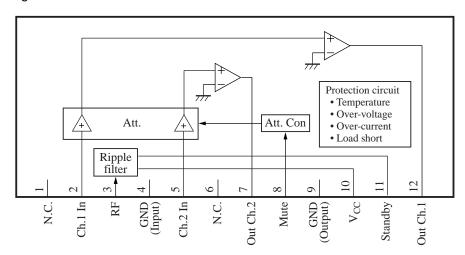
■ Features

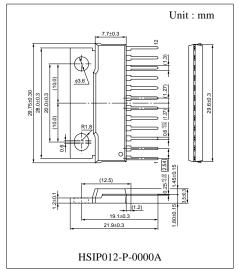
- Few external components :
 - No Boucherot cells(output C, R)
 - No Bootstrap Capacitors
 - No Negative Feeback Capacitors
- Built-in muting circuit
- Built-in stand-by circuit
- Built-in various protection circuits (Load-short, thermal, over-voltage and current)
- High ripple rejection(55 dB)
- Compatible with AN5275, AN5277
- Operating voltage range 10 V to 24 V(19 V typ.)

■ Applications

• TV

Block Diagram





■ Pin Descriptions

Pin No.	Descriptions	Pin No.	Descriptions
1	N.C.	7	Ch.2 output
2	Ch.1 input	8	Mute
3	Ripple filter	9	Output GND
4	Input GND	10	V _{CC}
5	Ch.2 input	11	Standby
6	N.C	12	Ch.1 output

■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	V _{CC}	26.0	V
Supply current	I_{CC}	4.0	A
Power dissipation *2	P_{D}	37.5	W
Operating ambient temperature *1	T_{opr}	-25 to +75	°C
Storage temperature *1	T_{stg}	-55 to +150	°C

Note) *1: Except these items, all other measurements are taken at $T_a = 25$ °C.

■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	10.0 to 24.0	V

^{*2:} $T_a = 75 \, ^{\circ}C$.

\blacksquare Electrical Characteristics at V_{CC} = 19 V, f = 1 kHz, R_L = 8 $\Omega,$ T_a = 25 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent current	I_{CQ}	$V_{IN} = 0 \text{ mV}$	_	35	70	mA
Output end noise voltage *1	V _{NO}	No input, $R_g = 10 \text{ k}\Omega$	_	0.22	0.4	mV
Voltage gain	G_{V}	$V_{IN} = 57 \text{ mV}$	32	34	36	dB
Total harmonic distortion	THD	$V_{IN} = 57 \text{ mV}$	_	0.2	0.4	%
Maximum Output Power	Po	V _{CC} = 22 V, THD = 10 %	5.6	7.0	_	W
Maximum Output power	Po	V _{CC} = 19 V, THD = 10 %	4.0	5.0	_	W
Ripple rejection ratio *1	RR	$V_r = 1 V_{rms}$ $f_r = 120 \text{ Hz}, R_g = 10 \text{ k}\Omega$	45	55		dB
Channel balance	СВ	$V_{IN} = 57 \text{ mV}$	-1.0	0	1.0	dB
Muting Ratio	MR	$V_{IN} = 57 \text{ mV}$	70	80	_	dB
Muting control voltage	V _{MUTE}	$V_{IN} = 57 \text{ mV}, MR \ge 70 \text{ dB}$	3.0	_	_	V
Standby control voltage 'on'	V _{STD-ON}	No input, $I_{CC} \le 0.1 \text{ mA}$	_	_	5.0	V
Standby control voltage 'off'	V _{STD-OFF}	No input, I _{CC} ≥ 17 mA	8.5	_	_	V
Channel crosstalk	СТ	$V_{\rm IN}$ = 57 mV, R_g = 10 k Ω	50	60	_	dB

Note) *1 : For this measurement, use the 20 Hz to 20 kHz (12 dB/OCT) filter.

■ Terminal Equivalent Circuits

Pin No.	Equivalent circuit	Description	DC voltage (V)
1	_	Not connected	_
2	2 200 Ω 400 Ω 30 kΩ	Ch.1 input This is the amplifier input pin.	0
3	30 kΩ	Ripple filter This is the pin to connect the positive terminal of a ripple filter capactior.	V _{CC} -1.5V _{BE}
4	_	Input GND Input ground pin	0
5	5 200 Ω 400 Ω 30 kΩ	Ch.2 input This is the amplifier input pin.	_
6	<u> </u>	Not connected	_

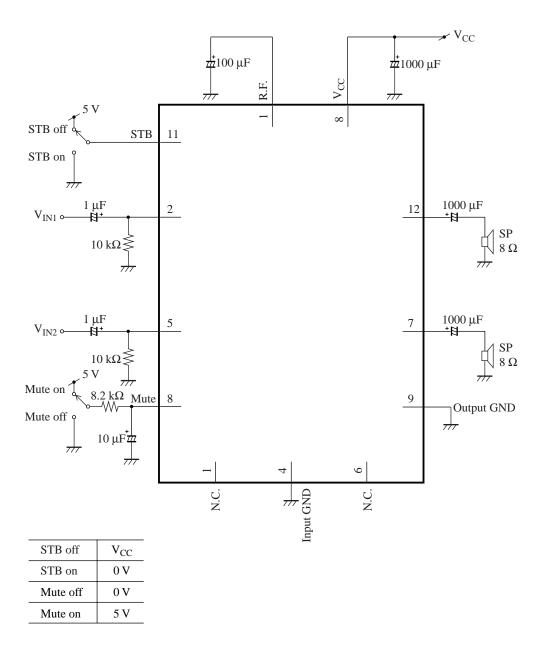
■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	DC voltage (V)
7	Pre amp. O	Ch.2 output Ch.2 output pin	V _{cc} /2
8	$\begin{array}{c} 10 \\ 3 \text{ k}\Omega \\ \hline \\ 10 \text{ k}\Omega \\ \hline \\ 4 \\ \end{array}$	Mute input pin. Mute 'on' = 5 V Mute 'off' = 0 V	
9	_	Output GND Ch.1 & Ch.2 output ground.	0
10	_	V _{CC} This is the power supply pin.	19 V(typ.)

■ Terminal Equivalent Circuits (continued)

Pin No.	Equivalent circuit	Description	DC voltage (V)
11	10 11 5 kΩ 5 kΩ 3 kΩ	Standby This is the standby control pin.	
12	Pre amp. 10 600Ω $30 k\Omega$	Ch.1 output Ch.1 output pin	V _{CC} /2

■ Application Circuit Example



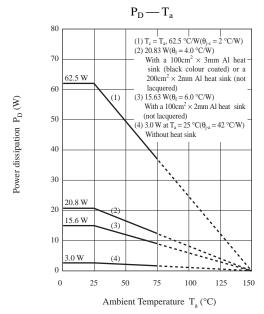
■ Usage Notes

- 1) External heatsink is needed when used. External heatsink should be fixed to the chassis.
- 2) Fin of the IC can be connected to GND.
- 3) Please prevent output to V_{CC} short and output to GND short.
- 4) The temperature protection circuit will operate at T_j around 150 °C. However, if temperature decreass, the protection circuit would automatically be deactivated and resume normal operation.

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■ Technical Information

• P_D — T_a curves of HSI P012-P-0000A



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